

REMARKS

The Office Action of August 13, 2004 has been carefully considered. In response thereto, the claims have been amended as set forth above. Reconsideration and allowance in view of the foregoing amendments and the following remarks is respectfully requested.

The objection to the drawing is noted. REPLACEMENT SHEETS are submitted herewith.

The claims were rejected/objeted to by reason of various informalities. These have been addressed by the present amendment.

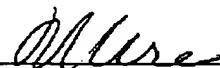
Claims 1-3 and 5-7 were rejected as being anticipated by Walters. The claims have been amended to more clearly distinguish over the cited references. Reconsideration is respectfully requested.

In particular, the claim 1 has been amended to recite that the Virtual Machine Interpreter comprises a pre-processor including a program counter, separate from the processor core. Walters is not believed to teach or suggest such a feature.

Claim 5 has been amended to recite, for selected virtual machine instructions that are not expected to be executed repeatedly, providing directly to the processor core instructions to implement the selected virtual machine instructions, without storing the instructions to implement the selected virtual machine instructions in memory. No such manner of operation is possible in Walters. In a software solution of the type described in Walters, besides being much slower, the processor cannot stream translated instructions to itself without the instructions being stored in memory.

Withdrawal of the rejection and allowance of claims 1-7 is respectfully requested.

Respectfully submitted,



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Dated: February 14, 2005